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FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. 108339-09045	SERIAL NO. 09/654,587
LIST OF REFERENCES CITED BY APPLICANT <i>(Use several sheets if necessary)</i>			APPLICANT LOR et al.		
			FILING DATE September 1, 2000	GROUP	

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	AD	5,828,653	Oct. 27, 1998	Goss			Apr. 26, 1996
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	AH	5,748,631	May 5, 1998	Bergantino et al.			May 9, 1996
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	BF	5,898,687	Apr. 27, 1999	Harriman et al.			Jul. 24, 1996
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	BQ	EP0859492A2	Aug. 19, 1998	EPO			
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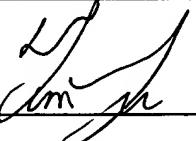
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	CK	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Jeng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
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	CM	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19
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EXAMINER		DATE CONSIDERED
1/14/04		
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>		